

ABSTRACT

A method and apparatus for a low jitter predriver for differential output drivers. In one embodiment, the predriver comprises a pull-up circuit having at least one pull-up device of a first device type and a pull-down circuit having at least one pull-down device of the first device type. In one embodiment, the pull-up circuit and the pull-down circuit to charge an output node and a complement output node in opposite directions to generate a differential predriver signal pair. Accordingly, using the pull-up and pull-down circuits, the predriver circuit generates differential output signals. In one embodiment, the pull-up device and the pull-down device comprise N-channel metal oxide semiconductor (NMOS) devices. Other embodiments are described and claimed.